

Notice of Allowability

Application N .

10/015,765

Examiner

Tarifur R Chowdhury

Applicant(s)

KOMATSU, HIROSHI

Art Unit

2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to supplemental amendment filed on 04/05/04.
2. ☒ The allowed claim(s) is/are 33-71.
3. ☒ The drawings filed on 17 December 2001 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). /
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☒ Certified copies of the priority documents have been received in Application No. 09/116,707.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Allowable Subject Matter

1. Claims 33-71 are allowed.
2. The following is an examiner's statement of reasons for allowance:
3. As to claims 33 and 69, the prior arts of record do not anticipate or render obvious to one skilled in the art an in-plane switching mode liquid crystal display comprising various elements as claimed, more specifically the common electrodes including a second capacitor portion overlapping the common line wherein the common electrode is formed on the passivation layer parallel to the data electrode and overlapping the gate and data bus lines and a first alignment layer on the common electrode.
4. As to claim 48, the prior arts of record do not anticipate or render obvious to one skilled in the art an in-plane switching mode liquid crystal display comprising various elements as claimed, more specifically common electrodes including parallel portions substantially parallel to the data bus line and a connecting portion substantially parallel to the gate bus line, the parallel portions extending from the connecting portion and having an oblique side, wherein the oblique side is located at a crossing of the data electrodes and common electrodes.
5. As to claim 64, the prior arts of record do not anticipate or render obvious to one skilled in the art an in-plane switching mode liquid crystal display comprising various

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elements as claimed, more specifically the common electrodes including a second capacitor portion overlapping the common line wherein the common electrode is formed on the passivation layer parallel to the data electrode, and a first alignment layer on the common electrode.

6. As to claim 27, the prior arts of record do not anticipate or render obvious to one skilled in the art an in-plane switching mode liquid crystal display comprising various elements as claimed, more specifically wherein the common electrode is formed on the passivation layer parallel to the data electrode, a light shielding layer on the common electrode and wherein the light shielding layer overlaps a portion of the gate bus line through a hole in the gate insulator and the passivation layer.

7. Since the priority date of the present application antedate the filing dates of the closest references USPAT 6,091,473 and USPAT 6,137,557 and applicant filed certified English translations for Korean Application Number 1997-34196 and Korean Application Number 1997-36569 to perfect the priority date, the above references are not valid prior art against the claims of the present application. Further, the newly cited reference USPAT 5,598,285 (Kondo) discloses an in-plane switching type liquid crystal display wherein a passivation layer is formed on the pixel electrode and the common electrode is formed on the passivation layer (Fig. 21). However, Kondo alone or in combination with any other valid prior art references fail to teach or suggest that the common electrodes including a second capacitor portion overlapping the common line as well as the common electrode overlapping the gate and data bus lines.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRC
April 29, 2004


TARIFUR R. CHOWDHURY
PRIMARY EXAMINER